

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention	SELECTIVE SILICON-ON-INSULATOR ISOLATION STRUCTURE AND METHOD																																																				
<p>Application Number:</p> <p>Confirmation Number:</p> <p>First Named Applicant: An Steegan</p> <p>Attorney Docket Number: FIS920030051</p> <p>Search string: (6429091 or 5153813 or 4849376 or 20020185469).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6429091</td><td>2002-08-06</td><td>Chen et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>5153813</td><td>1992-10-06</td><td>Oehrlein et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>3</td><td>4849376</td><td>1989-07-18</td><td>Balzan et al.</td><td></td><td></td><td></td></tr></tbody></table> <p>US Published Applications</p> <p>Note: Applicant is not required to submit a paper copy of cited US Published Applications</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Pub. No.</th><th>Date</th><th>Applicant</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>20020185469</td><td>2002-12-12</td><td>Podlesnik et al.</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><tr><td>Examiner Name</td><td>Date</td></tr><tr><td></td><td></td></tr></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6429091	2002-08-06	Chen et al.					2	5153813	1992-10-06	Oehrlein et al.					3	4849376	1989-07-18	Balzan et al.				init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass		1	20020185469	2002-12-12	Podlesnik et al.				Examiner Name	Date		
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